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APPLICANT: Belford T. Coursey	
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U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
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	AL						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AM							
	AN							
	AO							
	AP							
	AQ							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

	AR		Y. Kawamoto et al., "A 1.28 symbols μm^2 Bit-Line Shielded Memory Cell Technology for 64Mb DRAMs", *Central Research Laboratory*, CH2874-6/90/0000-0013, 1990 Symposium on VLSI Technology, 1990 IEEE.
	AS		
	AT		

EXAMINER: [signature] | DATE CONSIDERED: 6/10/02

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